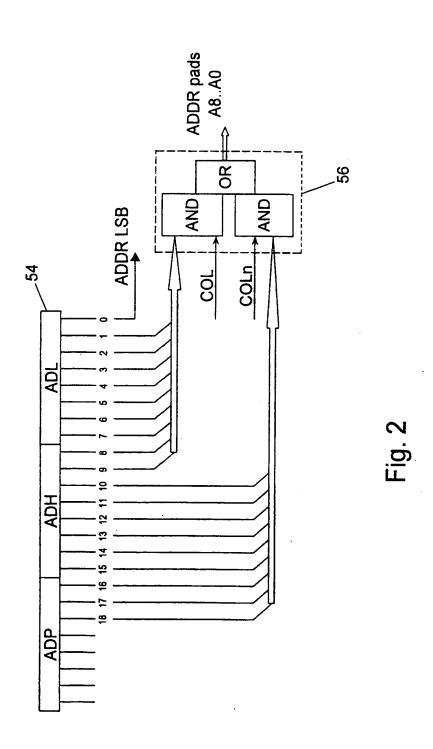
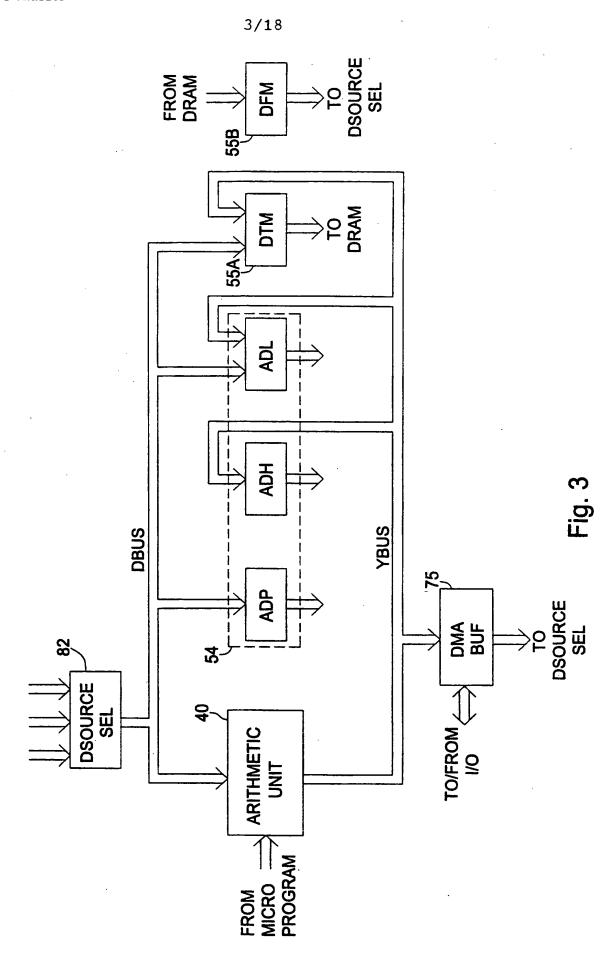
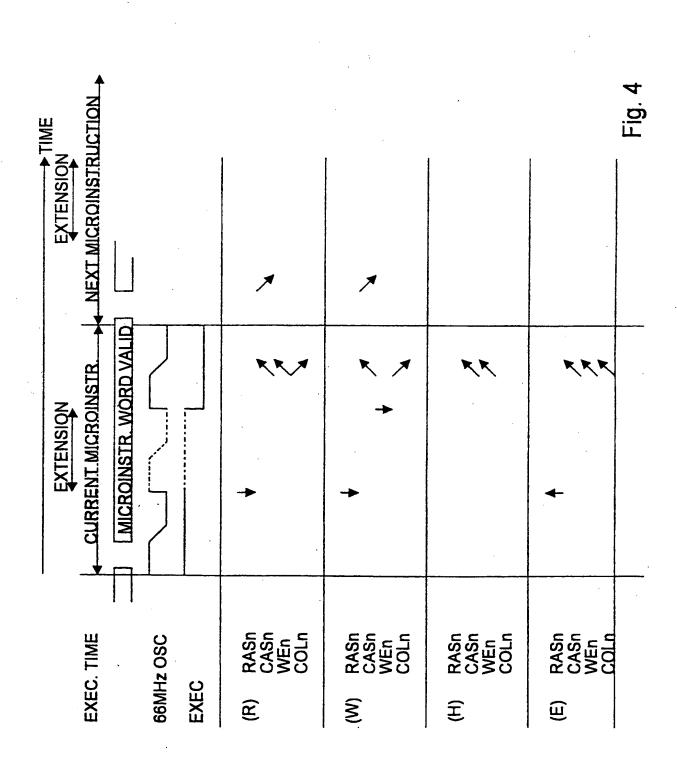


Fig. 1







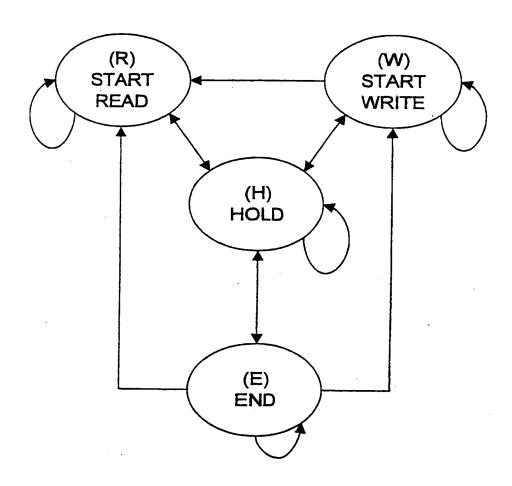
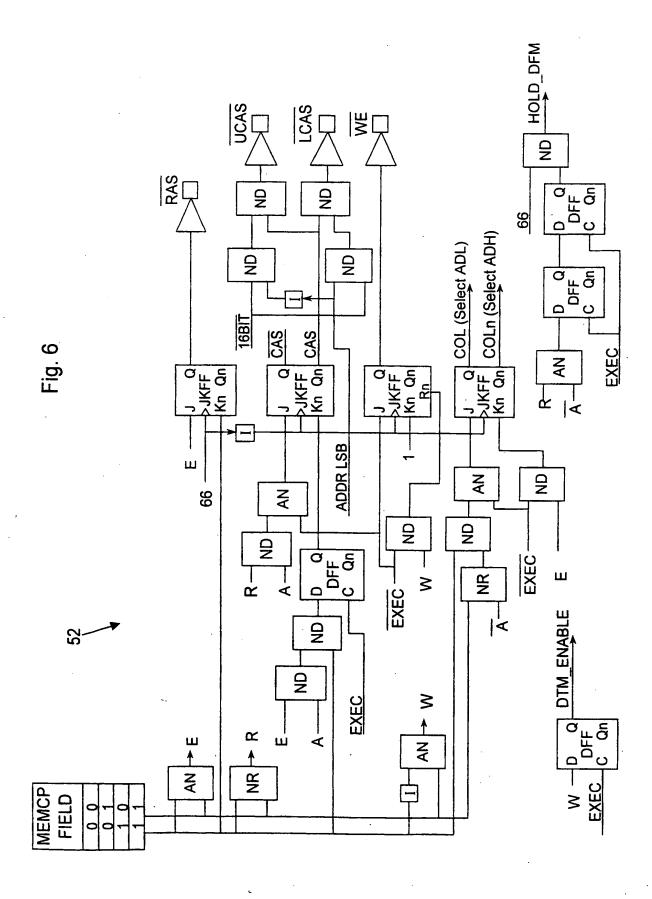
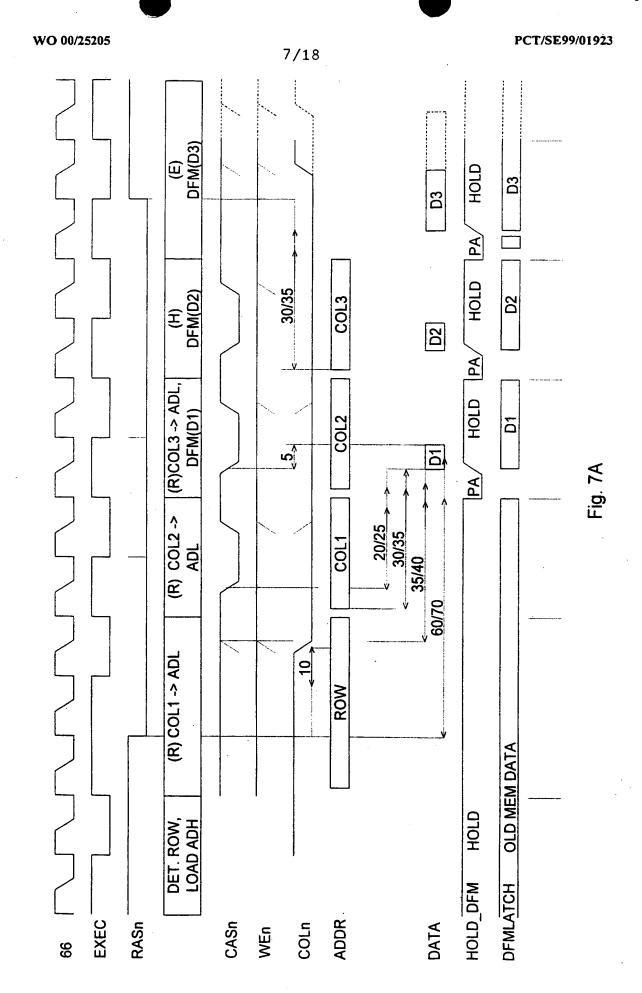
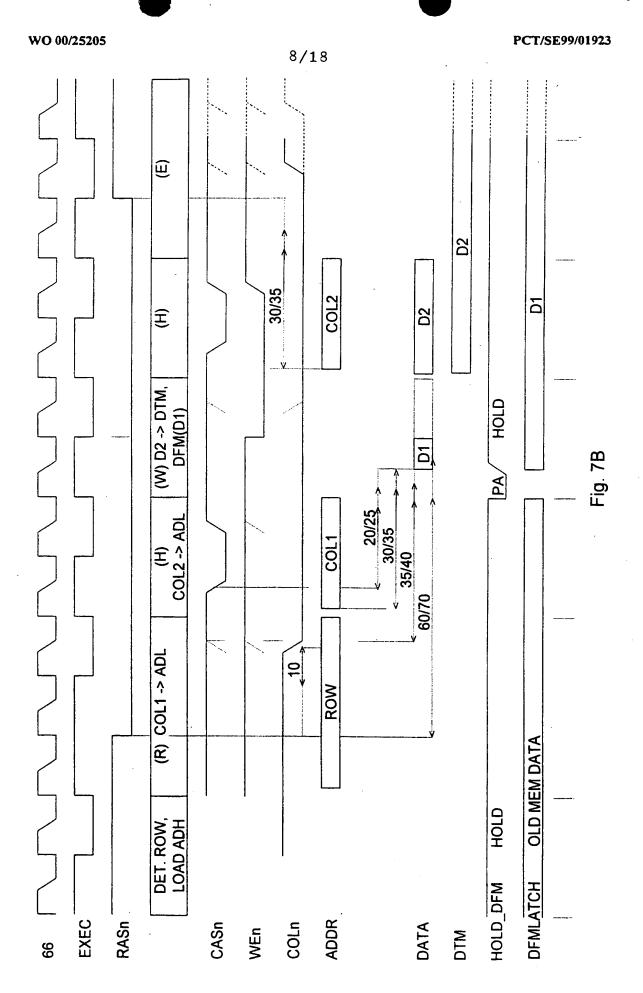


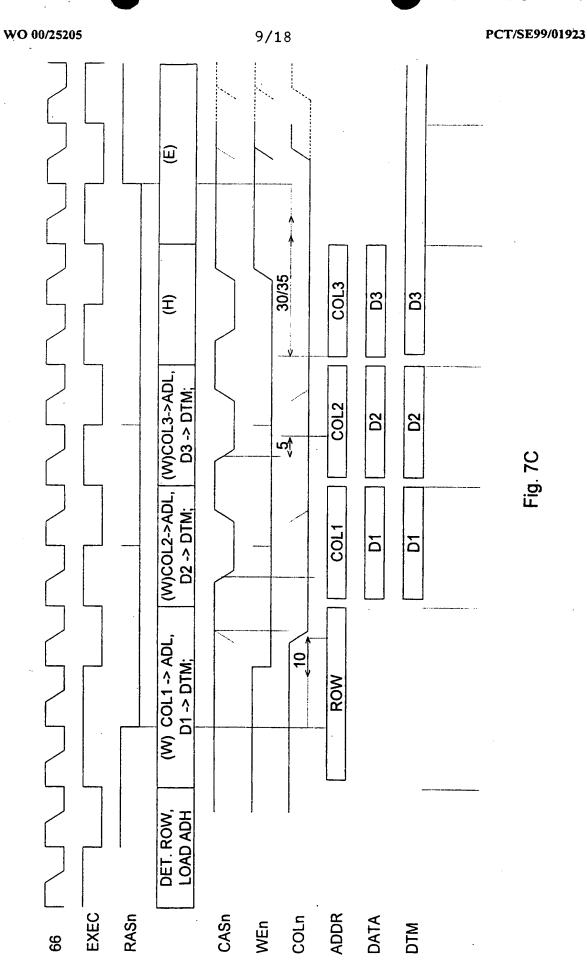
Fig. 5

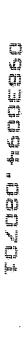


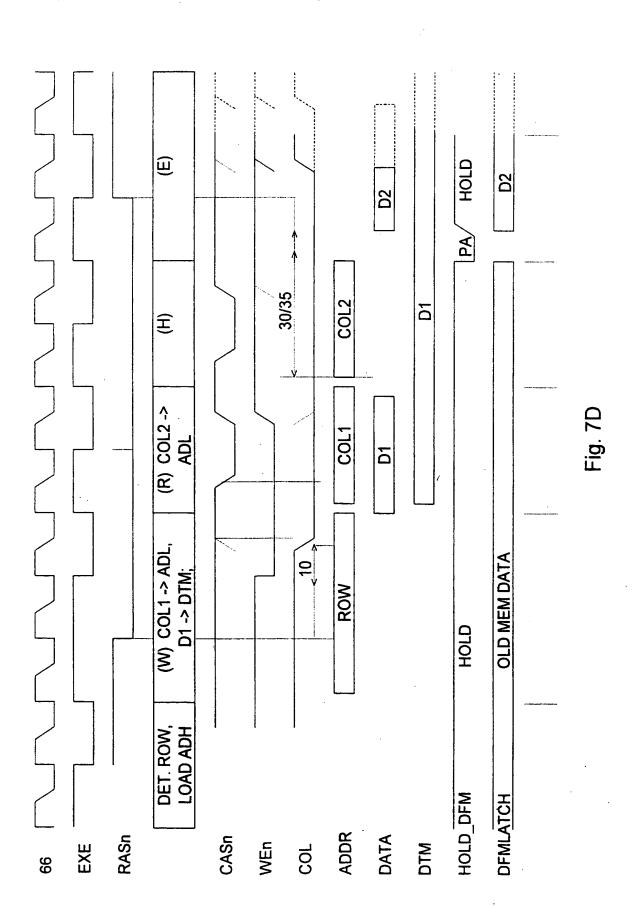










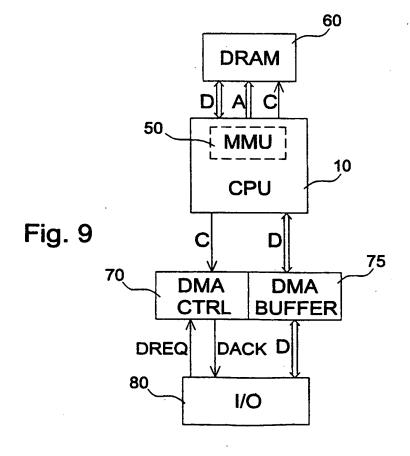


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Fig. 7E

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12/18 160 DRAM 150 **MMU** 110. CPU Fig. 8 HLDA (Prior art) HREQ 170 **DMA** CTRL DREQ DACK 180 1/0



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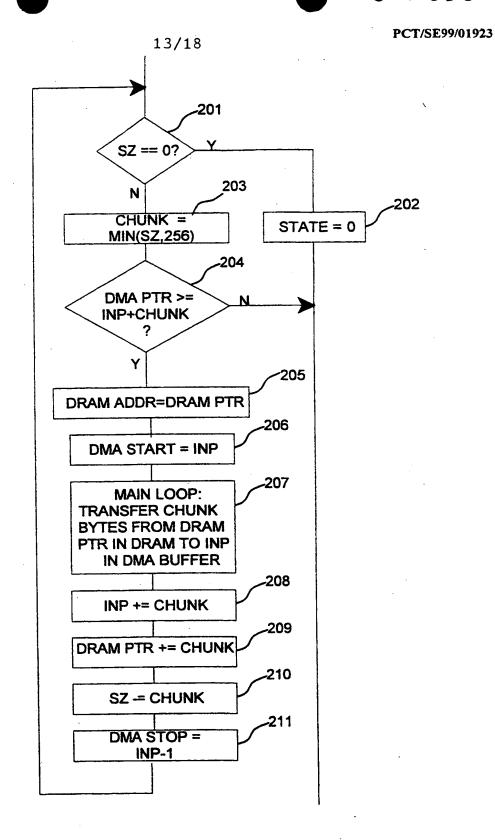
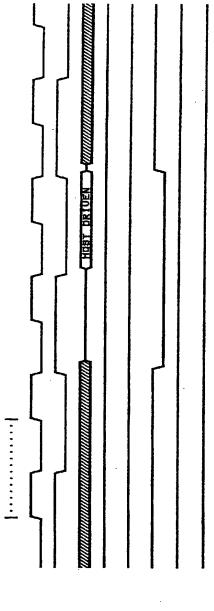


Fig. 10

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LIOA EXEC ICLK ID0..7 IDACKx ILIOA ILIOA ILIOA INEXT

WO 00/25205

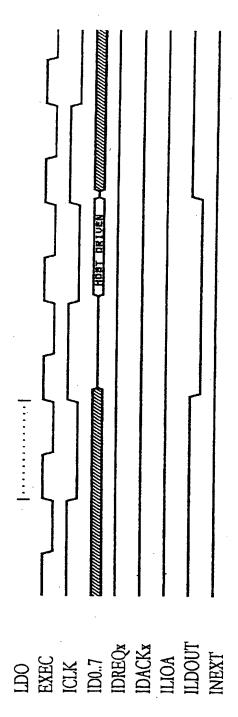


Fig. 11E

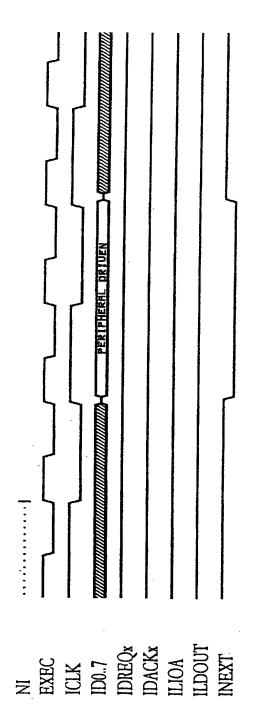


Fig. 11(

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ICLK
ID0.7
IDREQx
IDACKx
ILIOA
ILIOA
ILDOUT

Fig. 11[





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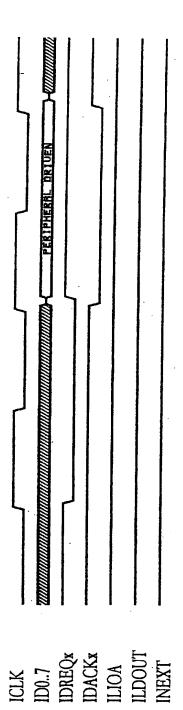


Fig. 11E